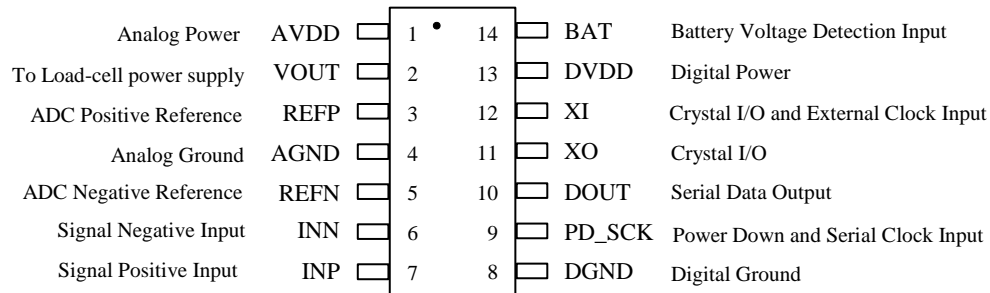


**Pin Description**


SOP-14L Package

Pin #	Name	Function	Description
1	AVDD	Power	Analog supply: 2.6 ~ 5.5V ( ≤ BAT )
2	VOUT	Analog Output	On chip power MOS switch output (NC when not used)
3	REFP	Analog Input	A/D conversion positive reference Input
4	AGND	Ground	Analog ground
5	REFN	Analog Input	A/D conversion negative Reference Input
6	INN	Analog Input	Differential negative input
7	INP	Analog Input	Differential positive input
8	DGND	Ground	Digital ground
9	PD_SCK	Digital Input	Power down control (high active) and serial clock input
10	DOUT	Digital Output	Serial data output
11	XO	Digital I/O	Crystal I/O (NC when not used)
12	XI	Digital Input	Crystal I/O or external clock input, 0: use on-chip oscillator
13	DVDD	Power	Digital supply: 2.6 ~ 5.5V ( ≤ BAT )
14	BAT	Analog Input	Battery voltage detection input (connect to higher of AVDD or DVDD when not used)

**Table 1 Pin Description**