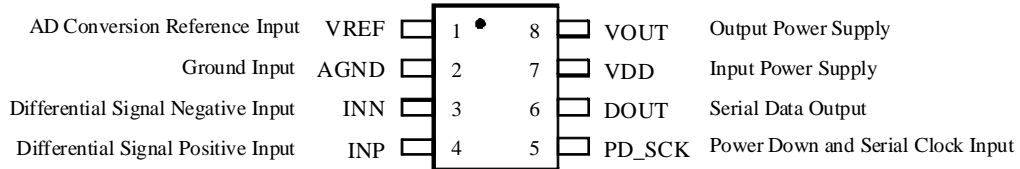


**Pin Description**


SOP-8 Package

Pin #	Name	Function	Description
1	VREF	Analog Input	Reference input voltage: 1.8 ~ 5.5V ( $\leq$ VDD)
2	AGND	Ground	Analog Ground
3	INN	Analog Input	Differential signal negative input
4	INP	Analog Input	Differential signal positive input
5	PD_SCK	Digital Input	Power down control (high active) and serial clock input
6	DOUT	Digital Output	Serial data output
7	VDD	Power Input	Input power supply: 2.6 ~ 5.5V
8	VOUT	Power Output	Output power supply, connected to VDD pin through an on-chip power switch

**Table 1 Pin Description**